

Abstract of the Disclosure

This invention describes a memory wear leveling for reducing wearing of hotspots (deteriorated memory blocks used more frequently) in all memory types by rotating the memory blocks on the physical level with the help of at least one spare
5 memory block using predetermined criteria during both read and write operations. The invention can be implemented e.g. by using constant memory pointers at a logical level and dynamic memory pointers on the physical level. The rotation can be implemented as a combination of software and hardware functionalities or using hardware or software alone.

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